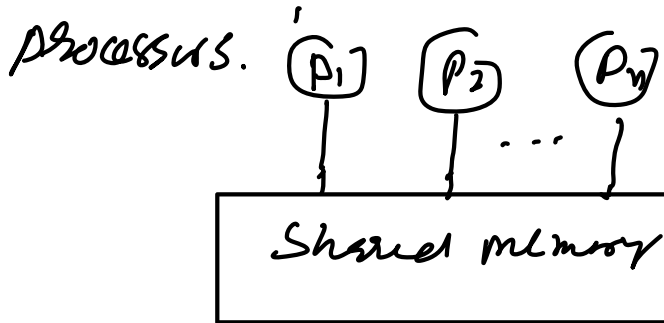


13-01-2022

Multiprocessor models + Communication Networks for parallel processing

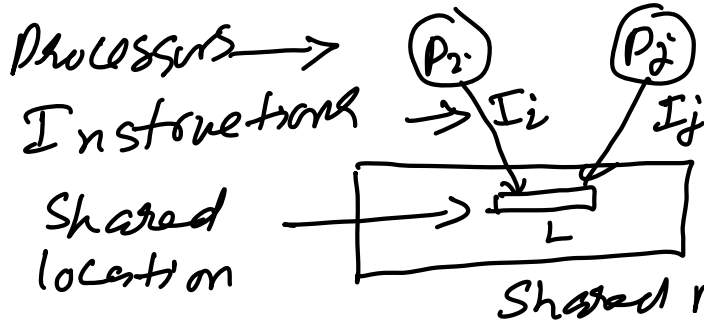
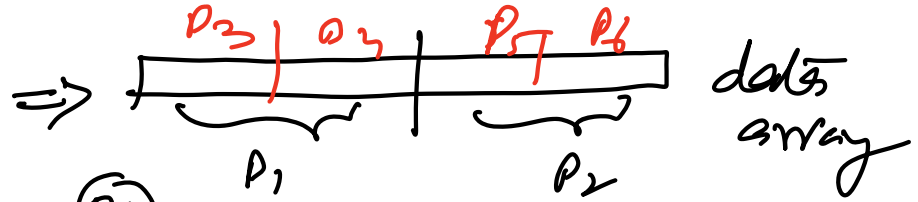
- 1) PRAM
- 2) LMM - local memory machine
- 3) MMM - modular memory machine

1) PRAM: Large memory and many
processors. memory is partitioned among



P_1, P_2, \dots, P_n are
physically located
close to each other.

Quick-sort can be run in parallel:



← Problem when there is simultaneous access.

- Exclusive Read Exclusive Write (EREW-PRAM)
(Implementation responsibility is of the algorithm designer)
- Concurrent Read Exclusive Write (CREW-PRAM):
(Implementation responsibility is that of algorithm designer)

If x is global: \Rightarrow

For example, $\left[\begin{array}{l} \text{for } P_i \Rightarrow x = 100; \\ \text{for } P_j \Rightarrow x = 500; \end{array} \right] ?$

- Concurrent Read Concurrent Write (CRCW-PRAM):

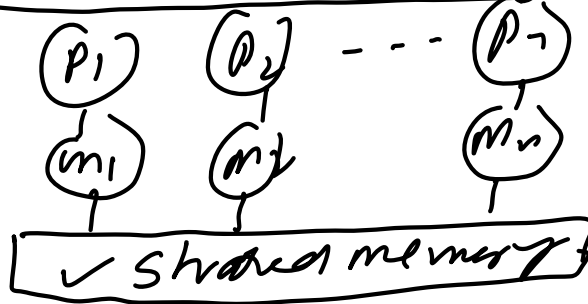
\therefore context is unpredictable task

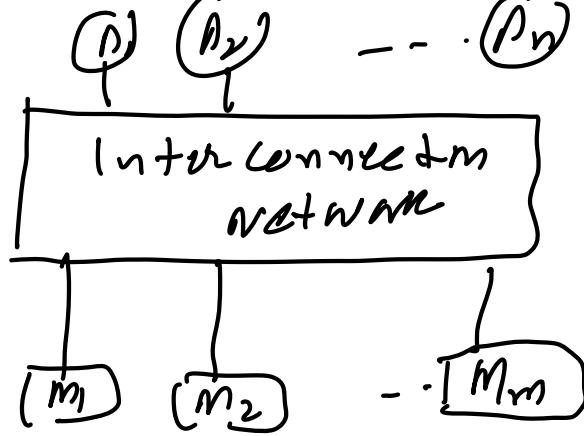
a) consistent \checkmark

b) Arbitrary \rightarrow only one is allowed

c) Priority $\bar{\quad}$

2) LMM - local memory machine





3) mmm : multistage memory machine

Adv: lot of flexibility, size no problem,

Challenges: - Complexity of network

- speed reduces.
- ∴ not so easily scalable
- ∴ performance reduces if network becomes very large

Specific challenges: -

- Routing properly can help to reduce complexity, (∴ there are routing algorithms)
- flow-control (algorithm)
- network topology (can be dynamically changed).

